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1. (Amended) A liquid crystal display, comprising:

a signal processor for generating and outputting a first image signal and a second image signal, a gray scale voltage, a gate voltage, and a driving control signal using an image data, a main control signal, and a power source all of which are supplied from an image supplying source, the driving control signal including a source driving control signal and a gate driving control signal;

a data signal driver for generating and outputting a data signal from the first image signal and the second image signal, the gray scale voltage and the source driving control signal all of which are input from said signal processor;

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a printed circuit board having a plurality of wires for transmitting the signals and/or voltages of said signal processor to said data signal driver;

a gate signal driver for generating and outputting a gate signal from the gate voltage and the gate driving control signal of said signal processor; and

a liquid crystal display panel for displaying an image formed by receiving the data signal from said data signal driver and the gate signal from said gate signal driver,

wherein the plurality of wires comprises a first group of wires for transmitting the first image signal and a second group of wires for transmitting the second image signal, and the first group of wires are entirely spaced apart from the second group of wires.